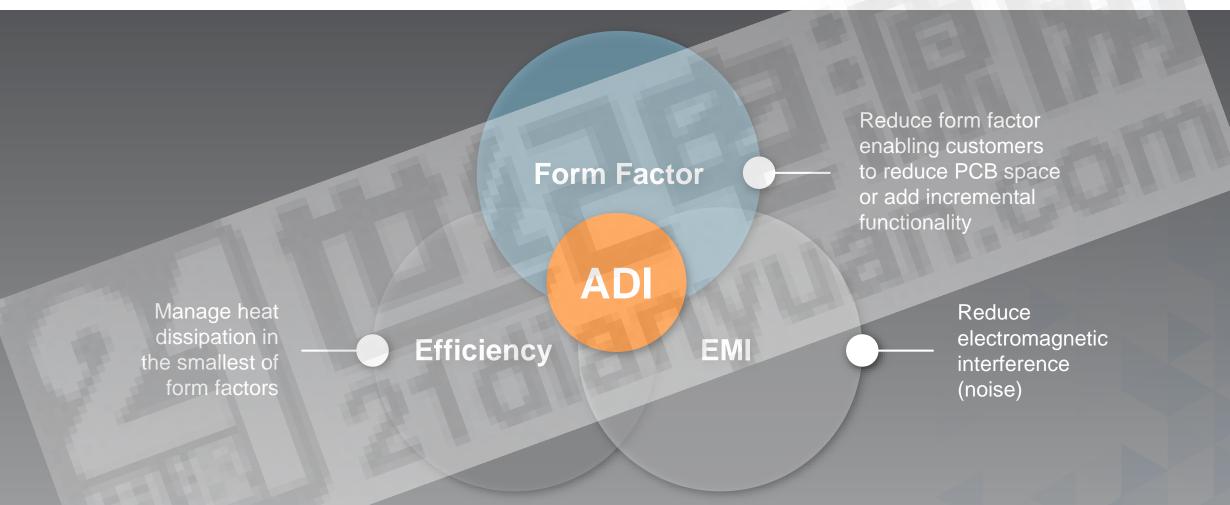
Low EMI power design ADI Silent Swither products 2019-6-29



AHEAD OF WHAT'S POSSIBLE™

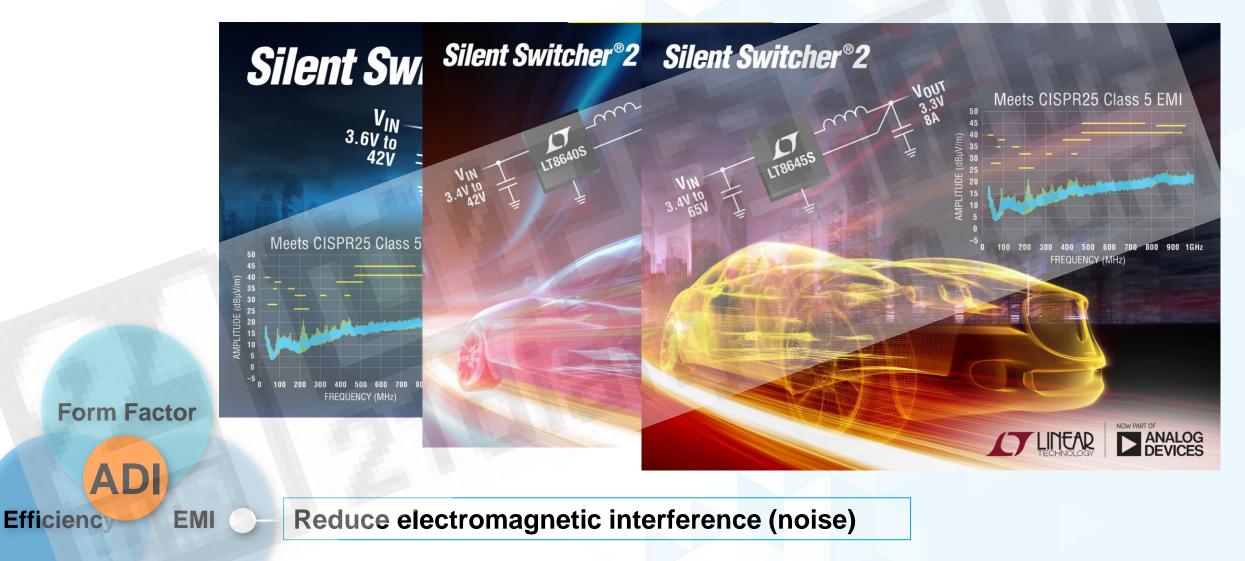
We solve the Three Key Power Supply Performance Metrics Form Factor, Efficiency, EMI





Power Innovation Technologies



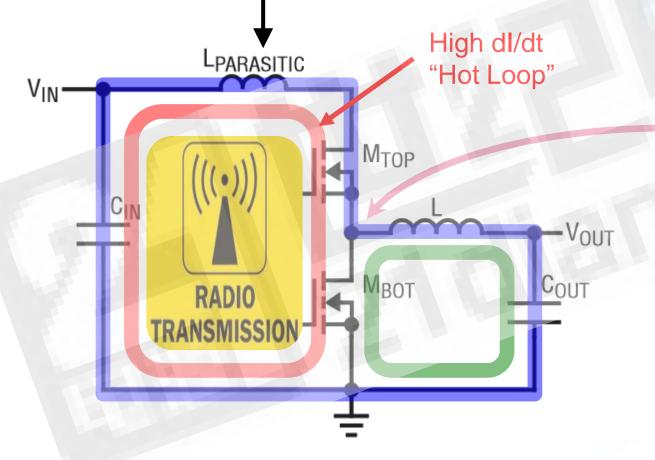


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The Problem in Traditional Synchronous Buck Converters High di/dt Currents in SMPS Hot Loops Create EMI



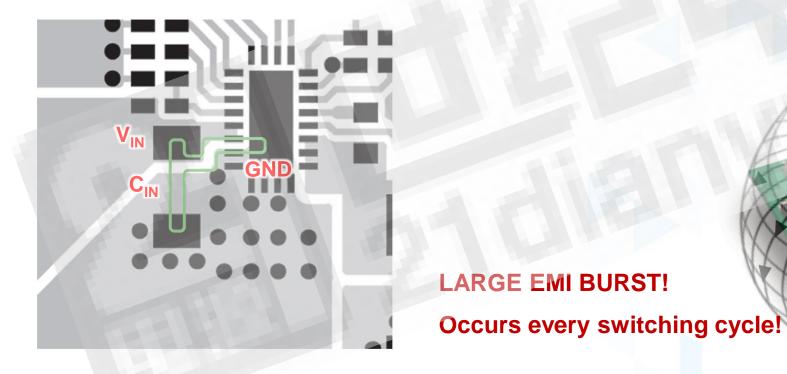
Parasitic inductance due to copper traces, bond wires, ESL of capacitors and FET internal metal



Excessive rings at the switching edges cause conductive noise and radiation

Minimizing Hot Loop

- With monolithic switcher, the best way is to place the low ESL input capacitor as close to the V_{IN} and GND as possible
- A solid GND plane with minimum distance to the hot loop is one of the most effective ways to reduce EMI

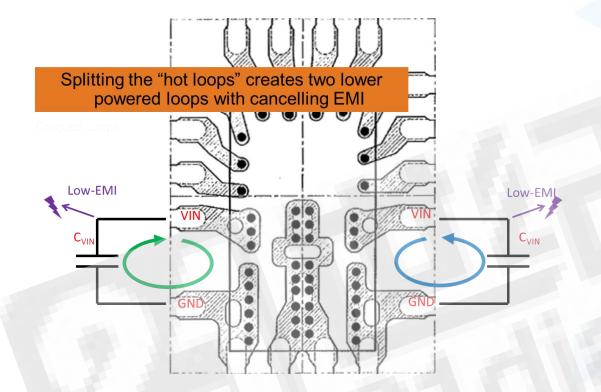




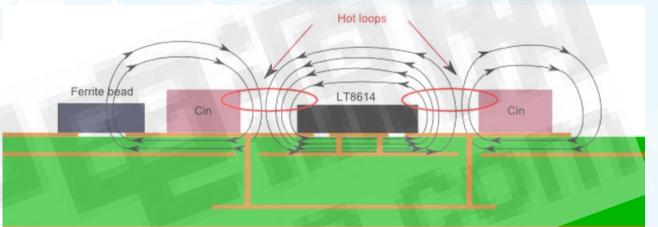
Silent Swither 1

Innovation - Silent Switcher

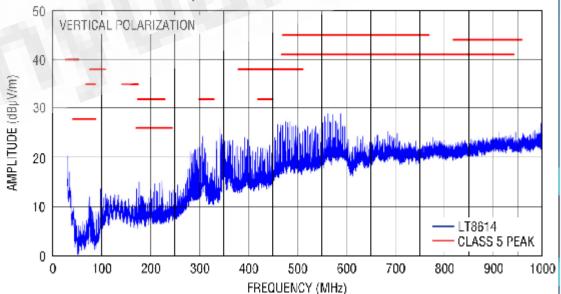




 The two high current loops cancel each others magnetic field, almost like enclosing the circuit in a metal box

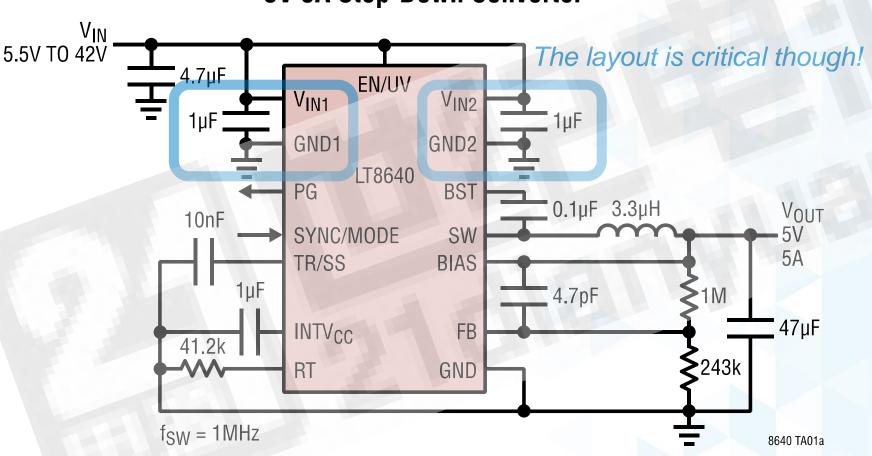


Radiated EMI Performance (CISPR25 Radiated Emission Test with Class 5 Peak Limits)

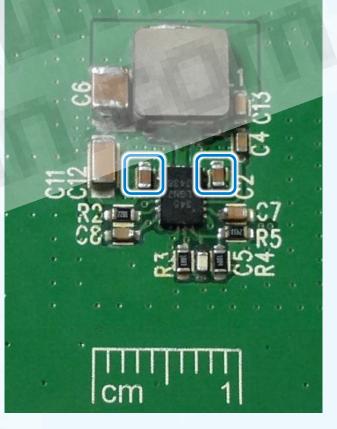


Silent Switcher 1





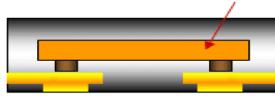
5V 5A Step-Down Converter

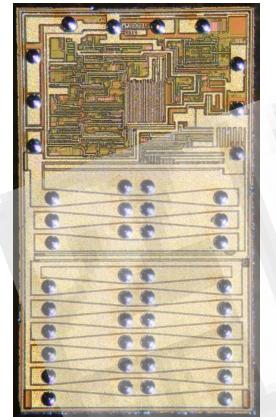


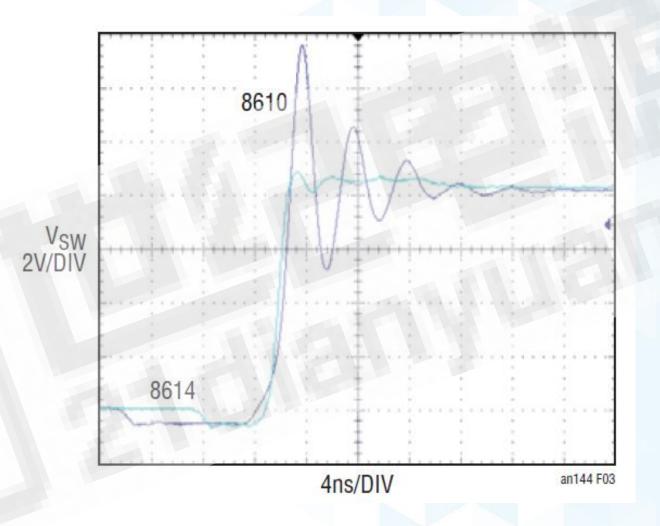
Silent Switcher Eliminates Switch Ringing



Silicon Die







LT8610: Wirebonded in MS16E

LT8614: Silent Switcher 1: Magnetic cancellation + CuPillar Flip-Chip

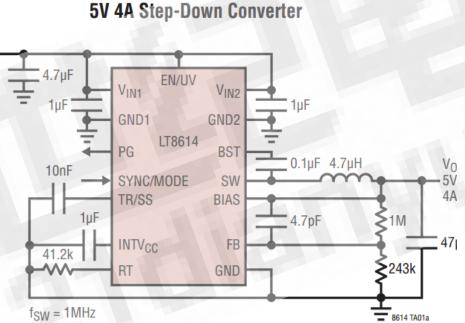
Silent Switcher Platform – Innovations To Deal with Hot Loop



- Buck regulator platform
 - 20dB EMI improvement No compromise in efficiency and size!

V_{IN} 5.8V TO 42V

- Offers customers:
 - High frequency
 - High efficiency
 - High current
 - Low EMI noise
 - Solder joint reliability
- Technologies
 - Circuits
 - Process/devices
 - Package
 - In-package passive



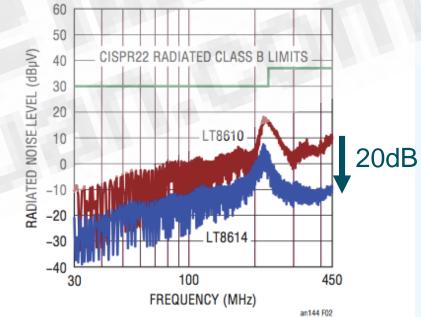


Figure 2. LT8610 and LT8614 700kHz 14V to 3.3V 2A Radiated EMI in GTEM Corrected for OATS

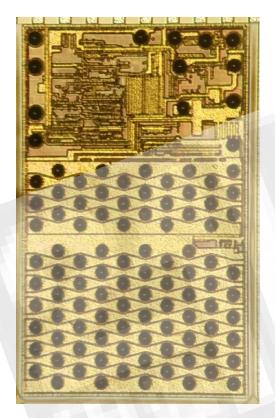


Silent Swither 2

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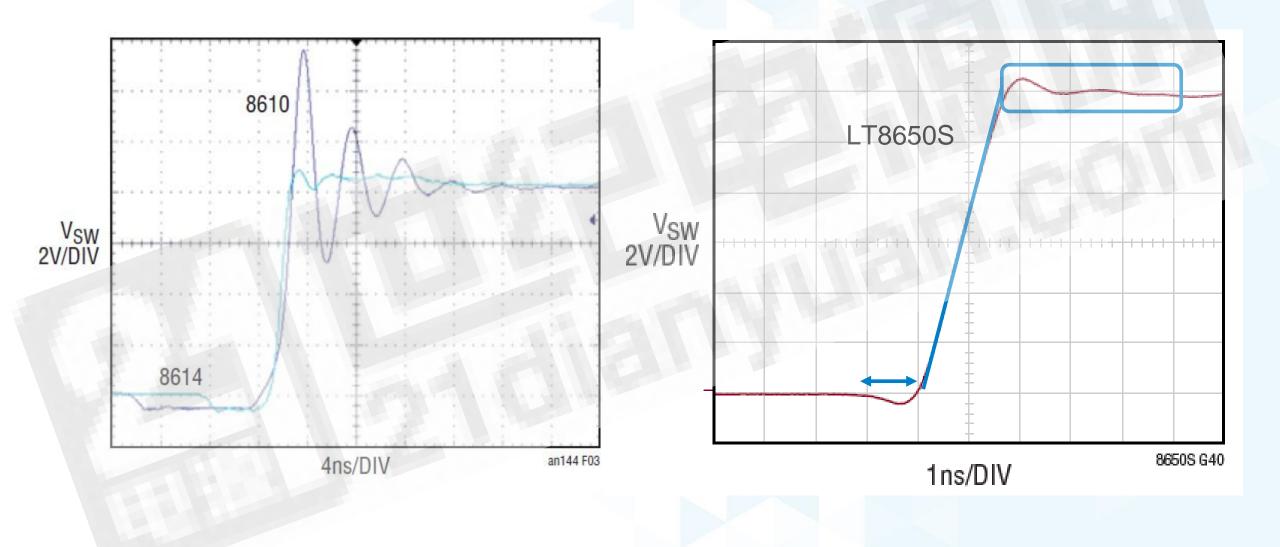
Silent Switcher 2: Flip chip on laminate (FCOL) and Cap-In-Package





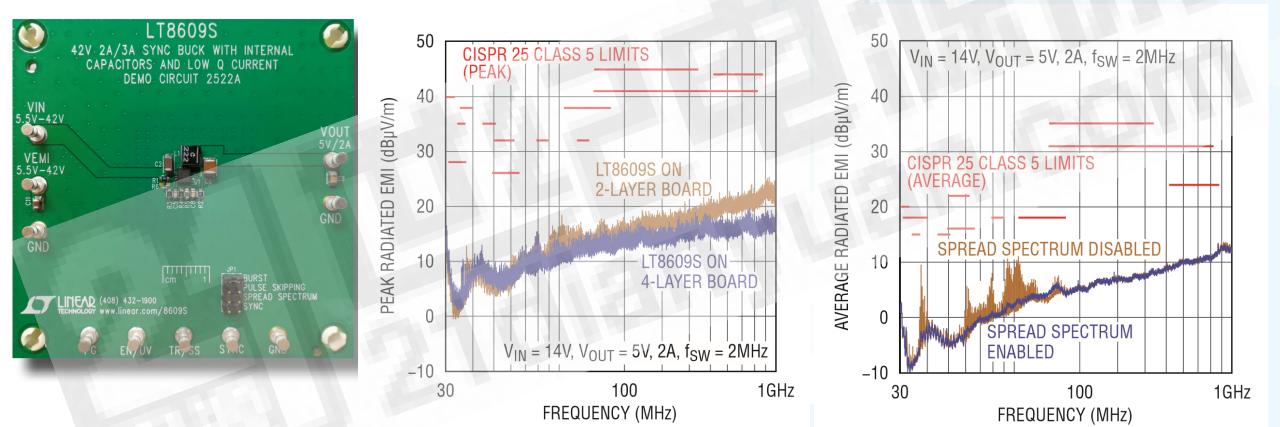
Imporve Performance





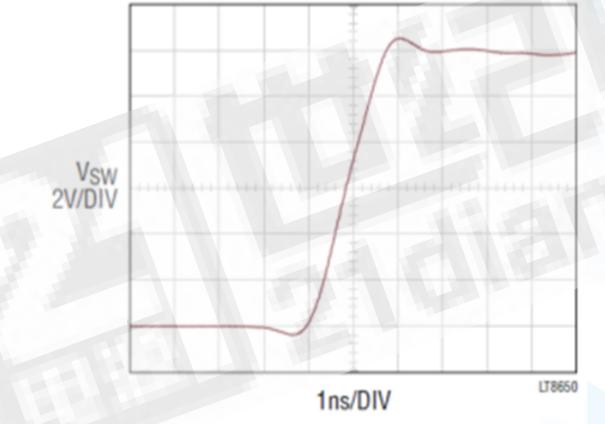
Silent Switcher 2 Example Excellent EMI Test Results





A Paradigm Shift Fast Switching Enables Ultralow EMI and High Efficiency

No slew rate limit on switching node necessary to achieve low EMI !



 Hot loop area and inductance virtually zero ANALOG

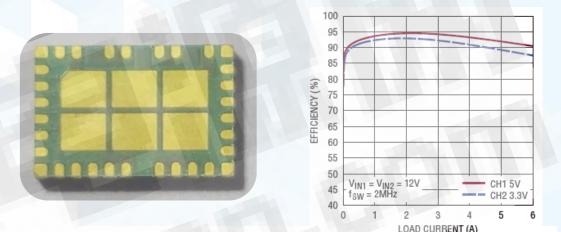
- Extremely fast switching
- Neglectable overshoot and no parasitic oscillation on switching node
- Dead time only 1ns

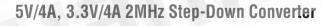
Unmatched switching performance !

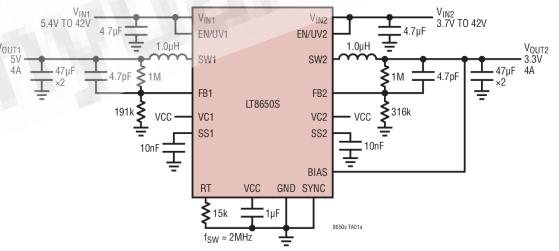
LT8650S Dual Channel 4A, 42V, Silent Switcher 2 Synchronous Buck



- Silent Switcher[®]2 architecture
 - Internal bypass capacitors reduce radiated EMI
 - Optional spread spectrum modulation
 - Ultralow EMI on any PCB, eliminate PCB layout sensitivity
- Wide input range: 3.0V to 42V
- High output dual capability:
 - 4A DC for each channel simultaneously
 - Up to 6A on either channel (limited by thermal)
- Ultralow quiescent current Burst Mode[®] operation
 - 6.2 μ A I_Q regulating 12V_{IN} to 5V_{OUT1} and 3.3V_{OUT2}
 - Output ripple <10mV_{P-P}
- Fast minimum switch-on time: 40ns
- Adjustable and synchronizable: 300kHz to 3MHz
- Small 4mm x 6mm 32-pin LQFN package



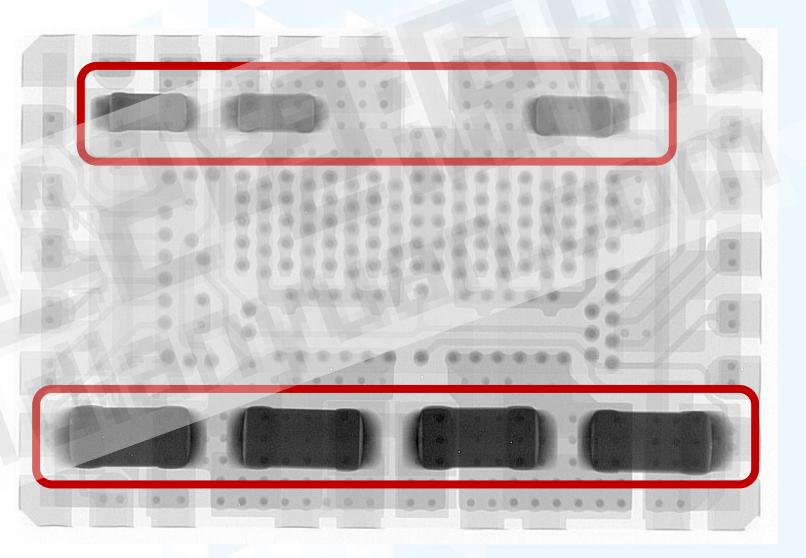




Silent Switcher 2 Platform

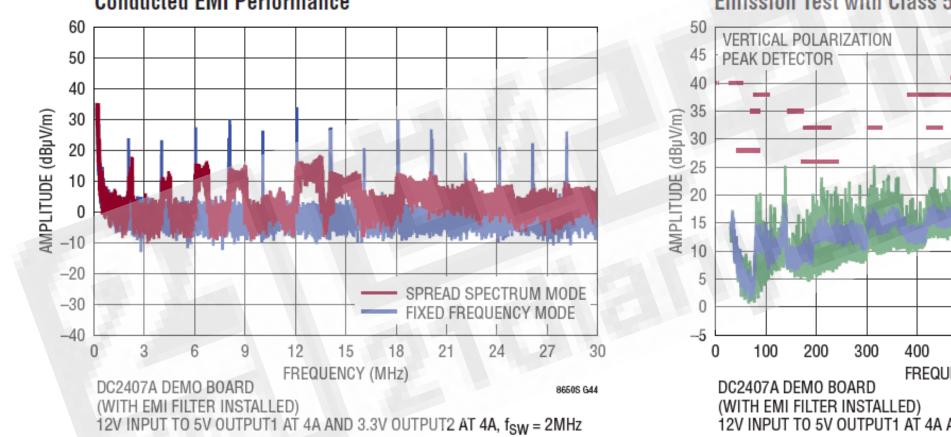


- Buck Regulator Platform
- Offers to the customer:
 - High Frequency
 - High Efficiency
 - High Current
 - Low EMI Noise
 - Solder Joint Reliability
- Tech:
 - Circuits
 - Process/Devices
 - Package
 - In-Package Passives



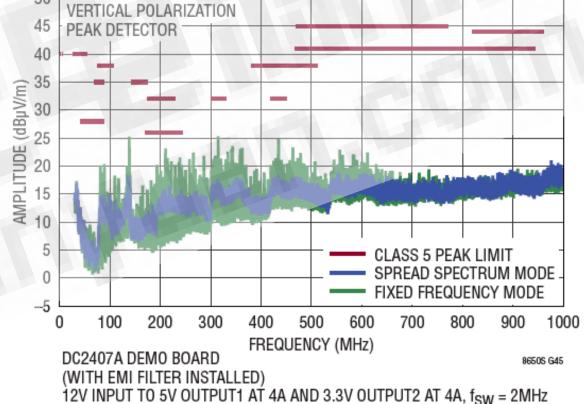
LT8650S Passes EMI Tests





Conducted EMI Performance

Radiated EMI Performance (CISPR25 Radiated Emission Test with Class 5 Peak Limits)



Silent Switcher Family



DEVICE	# OF OUTPUTS	V _{IN} RANGE	OUTPUT CURRENT	PEAK EFFICEINCY AT 2MHz, 12V TO 5V	lq	FEATURES	PACKAGES
LT8650S	2	3V – 42V	4A + 4A on both channels or 6A on either channel	94.60%	6.2µA	Silent Switcher 2	6mm x 4mm x 0.95mm LQFN
LT8645S	1	3.4V – 65V	8A	94%	2.5µA	Silent Switcher 2	6mm x 4mm x 0.95mm LQFN
LT8643S	1	3.4V – 42V	6A continuous 7A peak	95%	120µA	Silent Switcher 2, external compensation	4mm x 4mm x 0.94mm LQFN
LT8640S	1	3.4V – 42V	6A continuous 7A peak	95%	2.5µA	Silent Switcher 2	4mm x 4mm x 0.94mm LQFN
LT8609S	1	3V – 42V	2A continuous 3A peak	93%	2.5µA	Silent Switcher 2	3mm x 3mm x 0.94mm LQFN
LT8640 LT8640-1	1	3.4V – 42V	5A continuous 7A peak	95%	2.5µA	Silent Switcher, LT8640 pulse skipping, LT8640-1 forced continuous	3mm x 4mm QFN-18
LT8641	1	3V – 65V	3.5A continuous 5A peak	94%	2.5µA	Silent Switcher	3mm x 4mm QFN-18
LT8614	1	3.4V – 42V	4A	94%	2.5µA	Silent Switcher. Low ripple Burst Mode operation	3mm x 4mm QFN-18

Package technology improves performance, options for customer



3.32mm

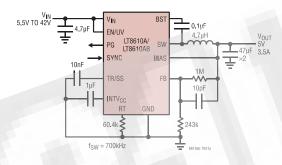
6.25mm

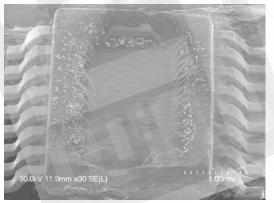
LT8610 MSOP-16





5V 3.5A Step-Down Converter

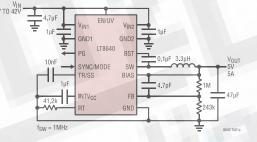


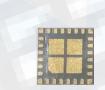




LT8640

5V 5A Step-Down Converter

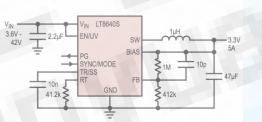




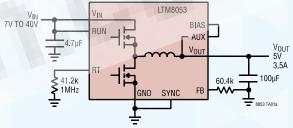
LT8640S

MSOP-16

(BT laminate LGA)







LTM8053

6x9 BGA



40Vin Step-Down µModule Regulator Size Comparison





40Vin Silent Switcher µModule Regulators

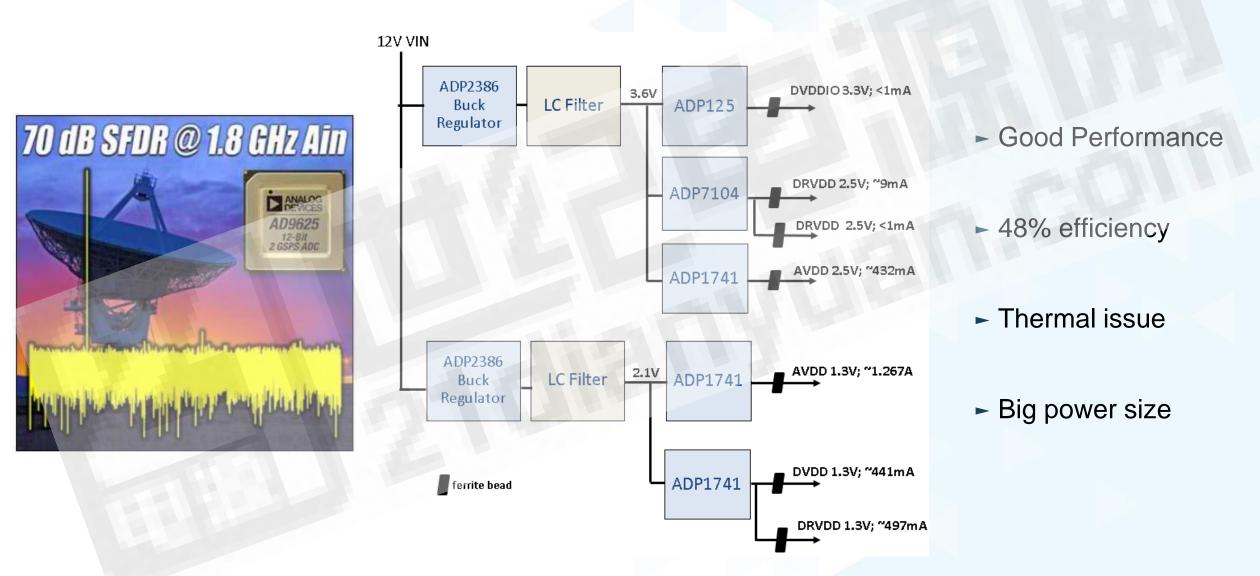


	LTM8074	LTM8063	LTM8065	LTM8053			
Silent Switcher		Ye	es				
CISPR22 Class B Compliant	Yes						
Vin Range	3.2V to 40V	3.2V to 40V	3.4V to 40V	3.4V to 40V			
Vout Range	0.8V to 12V	0.8V to 15V	0.97V to 15V	0.97V to 15V			
	1.2A (Continuous)	2A (Continuous)	2.5A (Continuous)	3.5A (Continuous)			
lout	1.75A (Peak)	2.5A (Peak)	3.5A (Peak)	6A (Peak)			
Switching Frequency		200kHz to 2.2MHz	200kHz to 3MHz	200kHz to 3MHz			
Package Size (mm)	4 x 4 x 2.22	4 x 6.25 x 2.22	6.25 x 6.25 x 2.32	6.25 x 9 x 3.32			
Package Type	BGA	BGA	BGA	BGA			
RPL Schedule	Q3 CY2018		Released	a			

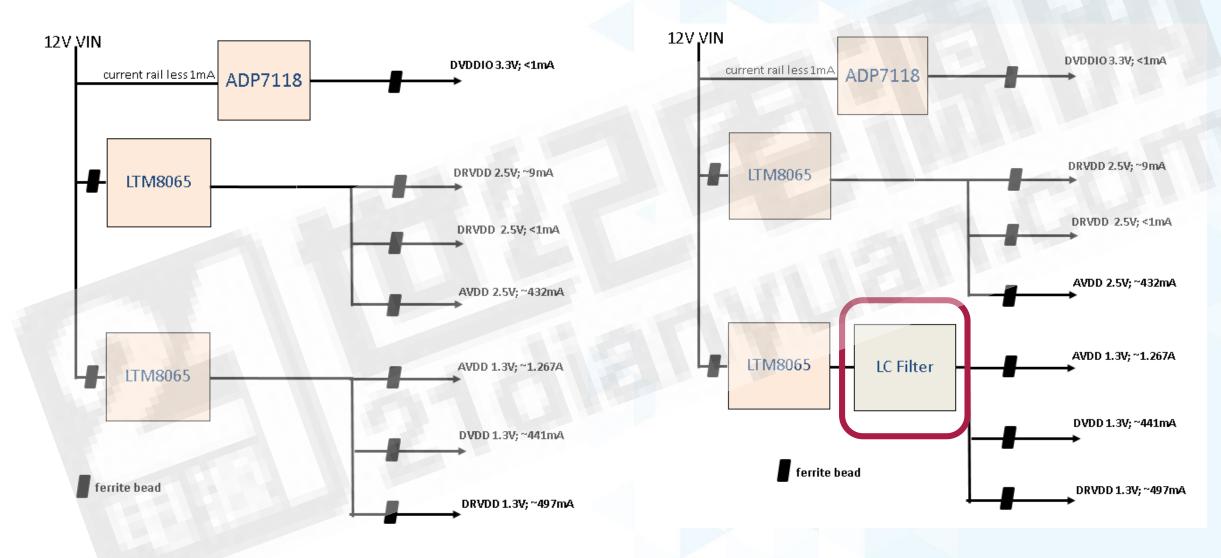


Application Improve System performance





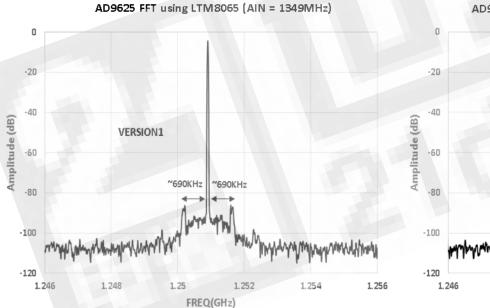




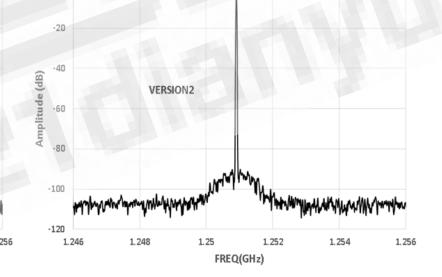


		AD9625-2.6 (GHz Dynamic	Performance			
Input Frequency		SNRFS (db)		SFDR (dbc)			
(MHz)	Baseline Power Supply	LTM8065 Version1	LTM8065 Version 2	Baseline Power Supply	LTM8065 Version1	LTM8065 Version 2	
729	57.01	57.03	57.01	79.87	79.72	80.11	
1349	56.53	56.49	56.54	78.41	80.06	80.77	

aseline Power Supply		Voltage (V)	(V) Current (A) Powe		
	PIN	11.729	0.676	7.929	
	AVDD_1.3V	1.268	1.222	1.549	
	DRVDD_1.3V	1.301	0.521	0.678	
Р	DVDD_1.3V	1.305	0.406	0.530	
0	AVDD_2.5V	2.589	0.408	1.056	
U	DRVDD_2.5V	2.590	0.0047	0.012	
Т	DVDD_2.5V	2.590	0.0001	0.0003	
	DVDDIO_3.3V	3.301	0.0004	0.0013	
			POUT TOTAL:	3.827	
			Efficiency (%):	48.26	







TM 8065 Version 2		Voltage (V)	Current (A)	Power (W)	
	PIN	11.885	0.442	5.256	
	AVDD_1.3V	1.303	1.308	1.704324	
	DRVDD_1.3V	1.302	0.531	0.691	
P	DVDD_1.3V	1.305	0.459	0.599	
0	AVDD_2.5V	2.486	0.440	1.094	
U	DRVDD_2.5V	2.494	0.005	0.012	
т	DVDD_2.5V	2.496	0.0001	0.0002	
	DVDDIO_3.3V	3.301	0.0004		
			POUT TOTAL:	4.102	
			Efficiency (%):	78.05	

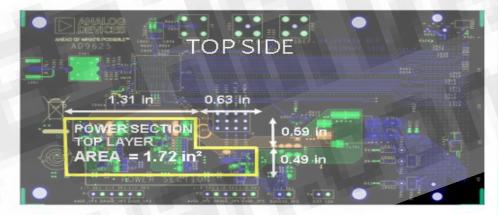


EVAL-AD9625 Evaluation Board



BOTTOM SIDE 1.14 lh POWER SECTION BOTTOM LAYER (SWITCHING REGULATORS) AREA = 3.47 in² 0.58 in 1.21 in 0.87 in 0.47 in

REVISED Evaluation Board USING LTM8065 POWER SOLUTION



Test Guaranteed VS Design Guaranteed



Features

LTC3803

- Low 20µA Quiescent Current
- +3.5V to +30V Wide Input Voltage Range, +45V Tolerant
- Operates Through Cold-Crank Conditions
- Low-Dropout Voltage of 280mV at 200mA
- Up to 200mA Output Current Capability
- Stable Operation with Tiny 4.7µF Output Capacitor.
- User-200mA, Automotive +3. **Quiescent Current**, +1. Re
- Open ELECTRICAL CHARACTERISTICS (Fixed (VIN = VENABLE = +14V. CTIMEOUT = ODED. CIN = 111

 - otherwise noted. Typical values are $T_A = +25^{\circ}C$.) (Note 2) PARAMETER SYMBOL CONDITIONS MAX UNITS MIN TYP SETOV INPUT (TRI-MODE) SET = HIGH, VSETOV = 5V SETOV Input Leakage Current uА or SET = GND, VSETOV = 5V SET = GND, VSETOV < VILSETOV or places SETOV Low-Level Input Voltage v VILSETOV 0.4 device in +3.3V fixed output-voltage mode SET = GND, VSETOV > VIHSETOV or places VOUT SETOV High-Level Input Voltage VIHSETOV v device in +5V fixed output-voltage mode - 0.4 Note 2: Production tested at T_A = +25°C. Overtemperature limits are guaranteed by design.

CAL CHARACTERISTIC The o denotes the specifications which apply over the full operating

CONTRACTOR FOR THE PARTY OF THE

QEOC V OV unless athenuise noted (Mate 2)

SYMBOL	PARAMETER	CONDITIONS	CONDITIONS			MAX	UNITS
V _{CLAMP1mA}	V _{CC} Shunt Regulator Voltage at 1mA	I _{CC} = 1mA, V _{ITH/RUN} = 0V LTC3803E LTC3803H, LTC3803I LTC3803MP	•	8.3 8.3 8	9.4 9.4 9.4	10.3 10.5 10.5	V V V
V _{CLAMP25mA}	V _{CC} Shunt Regulator Voltage at 25mA	I _{CC} = 25mA, V _{ITH/RUN} = 0V LTC3803E LTC3803H, LTC3803I LTC3803MP		8.4 8.4 8.1	9.5 9.5 9.5	10.5 10.7 10.7	V V V
VMARGIN	V _{CLAMP1mA} – V _{TURNON} Margin	LTC3803E LTC3803H, LTC3803I, LTC3803MP	•	0.05 0.03	0.6 0.6		V

High-

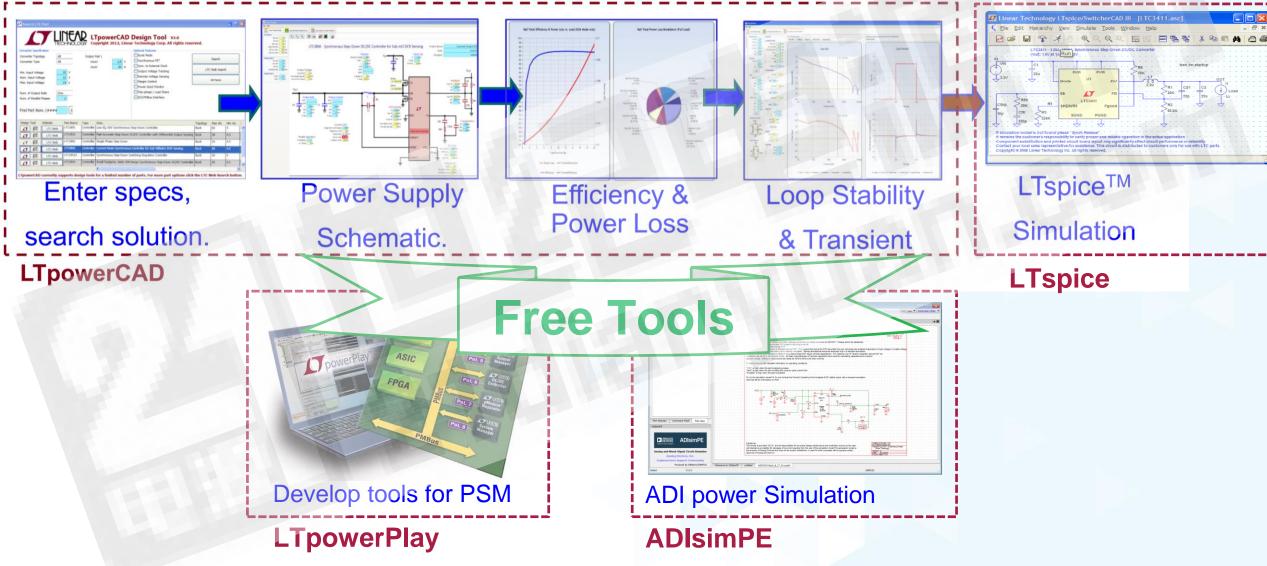
Thern

Opera

Autor

Design Tools: Step-by-Step Power Supply Design







Thanks

Analog Device

30 // 27 June 2019

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